



PATENT
03345-P0041A WWW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	Piotr Strzyzewski
Serial No. 10/601,508	Filing Date: June 23, 2003
Title of Application:	Method And Device For Treating Semiconductor Substrates
Confirmation No. 9726	Art Unit: 1762
Examiner	

Commissioner for Patents
Post Office Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement by Applicant

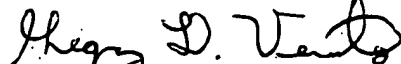
Dear Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR §1.56, Applicant lists the following references (copies of the listed patents enclosed).

U.S. Patent Documents				
Exam. Initials	Class/ Subclass.	Document No.	Date	Name
MDH	427/248.1	4,985,281	1/1991	Ahlgren
MDH	29/25.01	6,077,321	6/2000	Adachi et al.
MDH	204/298.01	6,328,864	7/2001	Ishizawa

Mailing Certificate: I hereby certify that this correspondence is today being deposited with the U.S. Postal Service as *First Class Mail* in an envelope addressed to: Commissioner of Patents and Trademarks; Post Office Box 1450; Alexandria, VA 22313-1450.

December 19, 2003

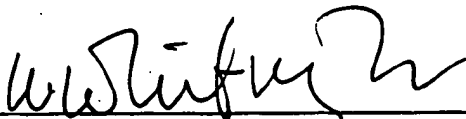

Gregory D. Venuto

Foreign Patent Documents			
Exam. Initials	Document No.	Date	Country
MDH	0209150	1/1987	EP
MDH	3193877	8/1993	JP (with English abstract)
MDH	5259098	10/1993	JP (with English abstract)
MDH	7058093	3/1995	JP (with English abstract)
MDH	10303279	11/1998	JP (with English abstract)

The listed patents pertain in a general way to the subject matter of the application, but are not necessarily considered to be analogous prior art.

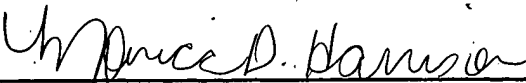
Respectfully submitted,

December 19, 2003



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August 6, 2004
Date Considered


Examiner